This listing of claims will replace all prior versions, and listings, of claims in the application:

LISTING OF THE CLAIMS:

1. (Canceled)

2. (Currently Amended) The method as claimed in Claim [[1]] 23, wherein said information collected relates to past, current and future input video frames comprising said sequence.

3.– 4. (Canceled)

5. (Currently Amended) The method as claimed in Claim [[3]] 23, further including the step of calculating a sufficient look ahead window for determining a size of said input buffer and correlating said size to said processing delay.

6. (Currently Amended) The method as claimed in Claim [[3]] 23, further including the step of: calculating a dynamic weighted picture complexity defined as a function of motion magnitude and picture quality index.

7. (Currently Amended) The method as claimed in Claim 5, further including the step of: determining a target bit allocation plan for the video frames so that available bits for all frames in the look ahead window may be used efficiently used.

8. (Original) The method as claimed in Claim 5, further including the step of: implementing rate control scheme to prevent underflow or overflow of a decode buffer requirement implemented according to a MPEG-2 standard.

9. (Original) The method as claimed in Claim 8, wherein the rate control scheme implements steps for adjusting the target bit allocation to prevent said decode buffer underflow or overflow.

10. (Currently Amended) The method as claimed in Claim 4, wherein the step [[c]] of encoding the incoming frames includes implementing said second encoder device for receiving said coding parameters, wherein said first encoder device collects direct video frame signal information and intermediate results in various frame encoding stages under same encoding operation conditions as employed by said second encoder device.

11. (Original) The method as claimed in Claim 10, wherein said first and second encoder devices operate at the same constant bit rate (CBR).

12. (Canceled)

13. (Currently Amended) The system as claimed in Claim [[12]] 24, wherein said information collected relates to past, current and future input video frames comprising said sequence.

14. – 15. (Canceled)

16. (Currently Amended) The system as claimed in Claim [[14]] 24, further including means for calculating a sufficient look ahead window for determining a size of said input buffer and correlating said size to said processing delay.

17. (Currently Amended) The system as claimed in Claim [[14]] 24, further including means for calculating a dynamic weighted picture complexity defined as a function of motion magnitude and picture quality index.

18. (Original) The system as claimed in Claim 16, further including means for determining a target bit allocation plan for the video frames so that available bits for all frames in the look ahead window may be used efficiently used.

19. (Original) The system as claimed in Claim 16, further including means for implementing rate control scheme to prevent underflow or overflow of a decode buffer requirement implemented according to a MPEG-2 standard, wherein the rate control scheme implements steps for adjusting the target bit allocation to prevent said decode buffer underflow or overflow.

20. (Currently Amended) The system as claimed in Claim 15, wherein said first encoder device collects direct video frame signal information and intermediate results in various frame encoding stages under same encoding operation conditions as employed by said second encoder device.

21. (Original) The system as claimed in Claim 20, wherein said first and second encoder devices operate at the same constant bit rate (CBR).

22. (Canceled)

23. (New) A method for real-time multi-pass encoding of a sequence of video frames comprising the steps of:

simultaneously feeding, in real-time, a sequence of incoming video frames to an input buffer and a first encoder device;

continuously collecting information, in real-time, on the statistics and rate-quality characteristics of said sequence of incoming video frames;

deriving a coding strategy to encode the sequence of incoming video frames based on the information collected;

generating coding parameters for instructing a second encoder device to encode said incoming frames according to said derived strategy; and,

encoding, by said second encoder device, the incoming frames by the derived coding strategy, wherein said input buffer implements a processing time delay of sufficient time such that sufficient information may be collected from said sequence by said first encoder device for deriving said coding strategy.

24. (New) A system for real-time multi-pass encoding of a sequence of video frames comprising:

an input buffer;

a first video encoder device;

a second video encoder device;

a means for simultaneously feeding, in real-time, a sequence of incoming video frames to said input buffer and first video encoder device, said first video encoder device for continuously collecting information, in real time, on the statistics and rate-quality characteristics of a sequence of input video frames and

means for deriving a coding strategy to encode the incoming frames based on the information collected; and,

means for generating coding parameters for instructing said second video encoder device to encode said incoming frames according to said derived strategy, said second video encoder for encoding the incoming sequence of frames according to the coding strategy,

wherein said input buffer means implements a processing time delay of sufficient time such that sufficient information may be collected from said input sequence by said first video encoder device for deriving said coding strategy.

25. (New) A program storage device readable by a machine, tangibly embodying a program of instructions executable by the machine to perform method steps for real-time multi-pass encoding of a sequence of video frames, said method steps including steps of:

simultaneously feeding, in real-time, a sequence of incoming video frames to an input buffer and a first encoder device;

continuously collecting information, in real-time, on the statistics and rate-quality characteristics of said sequence of incoming video frames;

deriving a coding strategy to encode the sequence of incoming video frames based on the information collected;

generating coding parameters for instructing a second encoder device to encode said incoming frames according to said derived strategy; and,

encoding, by said second encoder device, the incoming frames by the derived coding strategy, wherein said input buffer implements a processing time delay of sufficient time such that sufficient information may be collected from said sequence by said first encoder device for deriving said coding strategy.